

5V H bridge driver

Description

The MS31051 provides an integrated motor-driver, solution for cameras, consumer products, toys, and other low-voltage or battery-powered motion-control applications. The device has one H-bridge driver, and can drive one dc motor or one winding of a stepper motor, as well as other devices like solenoids.

The MS31051 can supply up to 1.2 A of output current. It operates on a motor power-supply voltage from 1.8 V to 6 V.

The MS31051 has a PWM (IN/IN) input interface which is compatible with industry-standard devices. There are internal over temperature protection.

Features

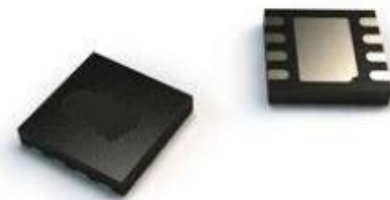
- H-Bridge Motor Driver
 - Drives a DC Motor or One Winding of a Stepper Motor or Other Loads
 - Low MOSFET On-Resistance: HS + LS 480 mΩ
- 1.2A MAX Drive Current
- motor supply VCC: 1.8~6V
- PWM (IN/IN) Interface
- over temperature protection
- Low-Power Sleep Mode (When IN1=IN2=0)

Product Information

Product	Package	Mark
MS31051S	SOT-23-6L	MS51S
MS31051D	DFN8	MS51D



SOT-23-6L



DFN8

Applications

- Cameras
- DSLR Lenses
- Consumer Products
- Toys
- Robotics
- Medical Devices

FUNCTIONAL BLOCK DIAGRAM

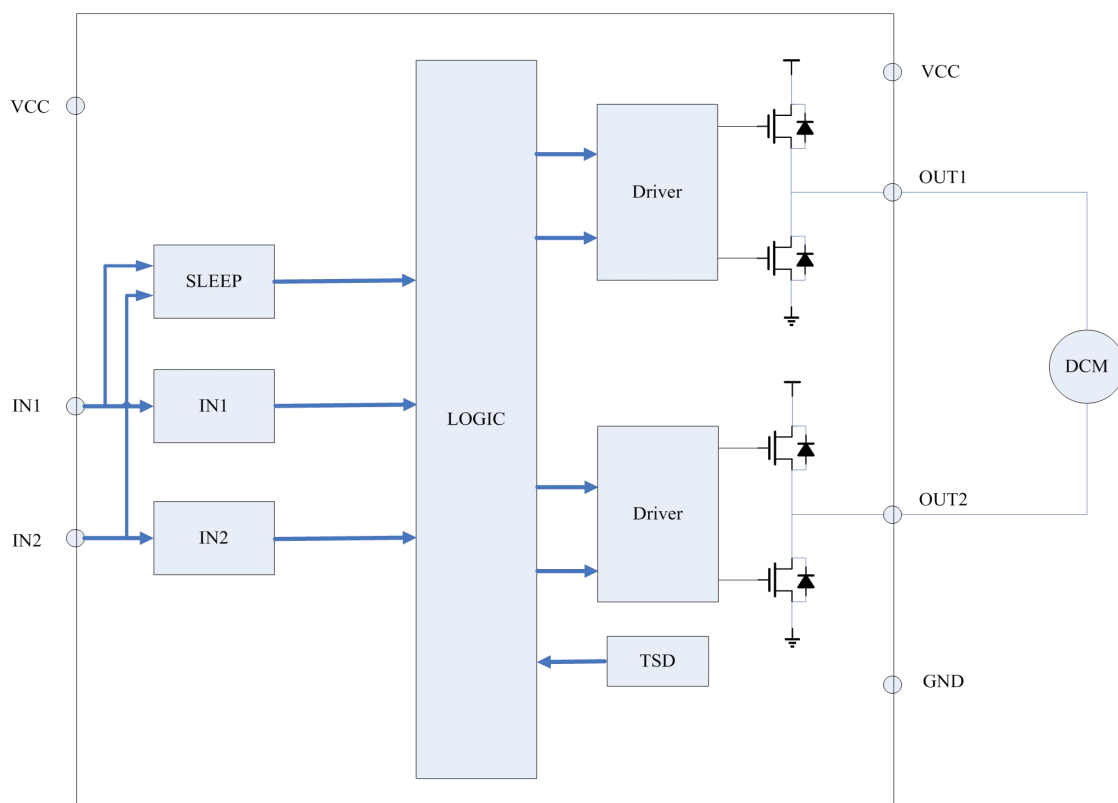


Figure 1 : MS31051 Functional block

Absolute Maximum Ratings

Parameter	Symbol	Range	UNIT
Power supply voltage range	VCC	-0.3~6	V
Digital pin voltage range	INx	-0.5~6	V
motor drive constant output current	I _{max}	0~1.2	A
Operating virtual junction temperature range	T _{jmax}	-40~150	°C
Storage temperature range	T _{stg}	-60~150	°C
ESD(human body)	ESD	±2500	V

Electrical Characteristics(unless otherwise noted, T=25°C, VCC=5V)

RECOMMENDED OPERATING CONDITIONS: (T=25°C, unless otherwise noted)

Parameter	Symbol	Min	Typ	Max	Unit
Device power supply voltage range	VCC	1.8		6	V
H-bridge output current	I _{OUT}	0		1.2	A
Device power supply voltage range	f _{PWM}	0		250	KHZ
Logic-level input voltage	V _{logic}	0		6	V
Working temperature range	T _a	-40		85	°C

ELECTRICAL CHARACTERISTICS: (T=25℃, VCC=5V, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
VCC supply voltage	VCC		1.8		6	V
VCC operating supply current1	IVCC	VCC=5V, No PWM		100	550	uA
VCC operating supply current2	IVCCQ	VCC=5V		0.01	1	uA
H-Bridge FETs						
HS + LS FET on-resistance	RdsON	VCC=5V, Io=500mA		850	1000	mΩ
Off-state leakage current	IOFF	Vout=0V	-200		200	nA
Logic-Level Inputs (IN1, IN2)						
Input low voltage	VIL				0.27*VCC	V
Input high voltage	VIH		0.4*VCC			V
Input hysteresis	VHY			0.13*VCC		mV
Input low current	IIL	Vin=0	-5		5	uA
Input high current	IIH	Vin=3.3V, pin INx			50	uA
Pulldown resistance	Rpd			100		kΩ

Protection Circuits						
Thermal shutdown temperature	TSD		150	160	180	℃
Thermal shutdown temperature hysteresis	ΔTSD			20		℃

TIMING REQUIREMENTS (TA = 25℃, VCC = 5 V, RL = 20 Ω)

parameter	Test conditions	range		unit
		min	max	
T1	Output enable time		80	us

T2	Output disable time		200	ns
T3	Delay time, INx high to OUTx high		100	ns
T4	Delay time, INx low to OUTx low		100	ns
T5	Output rise time	10	188	ns
T6	Output fall time	10	188	ns

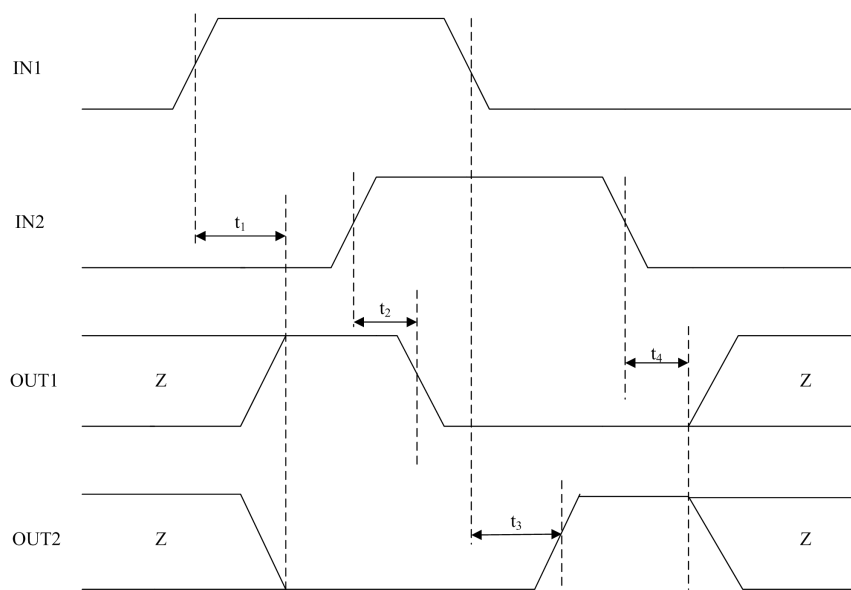


Figure 2 MS31051 Input/Output Timing1

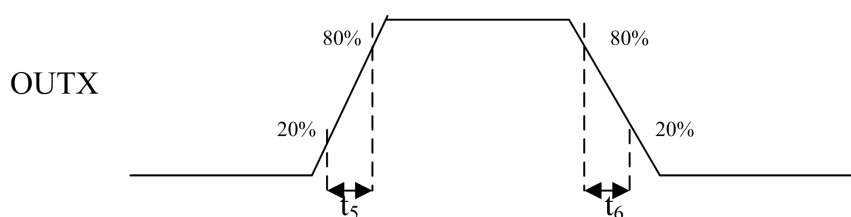
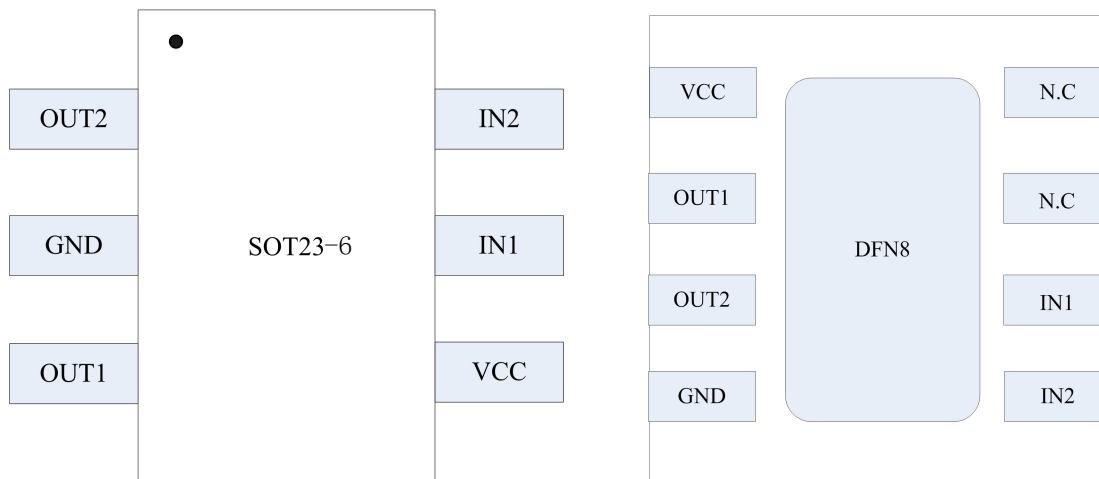


Figure 3 MS31051 Input/Output Timing2

Pin diagram



Pin Number		name	I/O	Pin Description
sot23 -6L	DFN8			
1	3	OUT2	O	输出 2
2	4	GND	gnd	地
6	5	IN2	I	输入 2
5	6	IN1	I	输入 1
4	1	VCC	power	Power 电源
3	2	OUT1	O	输出 1
	7,8	N.C	/	无连接

Functional Description

The MS31051 is controlled using a PWM input interface , also called an IN/IN interface. Each output is controlled by a corresponding input pin.

The following table shows the logic for the MS3010:

IN1	IN2	OUT1	OUT2	function
0	0	Z	Z	Sleep mode
0	1	L	H	Reverse
1	0	H	L	Forward
1	1	L	L	Brake

Sleep Mode

If the input pin IN1 ,IN2 are all set to logic-low state, the MS31051 enters a low-power sleep mode. In this state, all unnecessary internal circuitry is powered down.

Protection Circuits

If the die temperature exceeds safe limits, all FETs in the H-bridge are disabled. Once the die temperature has fallen to a safe level, operation automatically resumes. The MS31051 has thermal shutdown (TSD) as described in the Protection Circuits section. If the die temperature exceeds approximately 160°C, the device is disabled until the temperature drops to a safe level.

Any tendency of the device to enter thermal shutdown is an indication of either excessive power dissipation, insufficient heatsinking, or too high an ambient temperature.

protection	conditions	Hbridge state	Resume conditions
TSD	$T_j > 160^{\circ}\text{C}$	disable	$T_j < 140^{\circ}\text{C}$

Sleep Mode

If the nSLEEP pin is brought to a logic-low state, the MS31051 enters a low-power sleep mode. In this state, all unnecessary internal circuitry is powered down.

mode	nSLEEP	Hbridge state
Normal working	IN1, IN2 not all '0'	enable
Sleeping model	IN1=IN2=0	disable
TSD	INx=X	disable

Application circuit Example

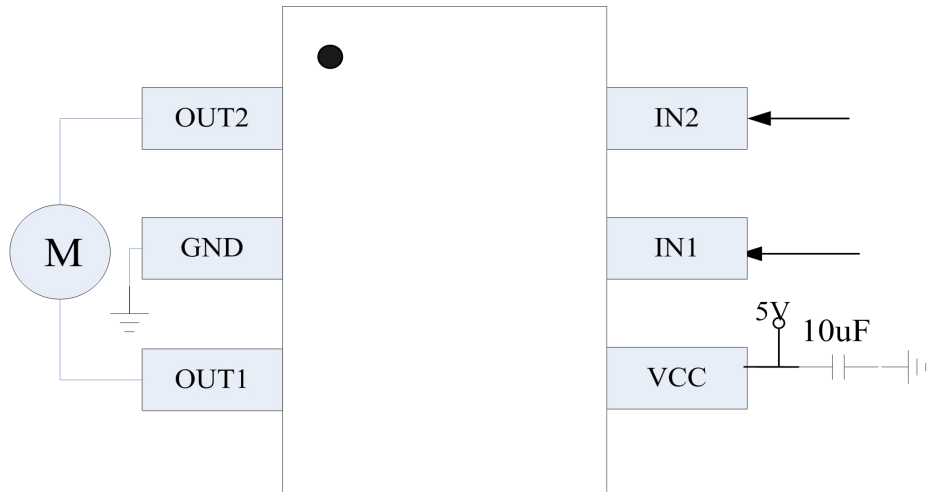


Figure 4A Application circuit Example for sot23-6L

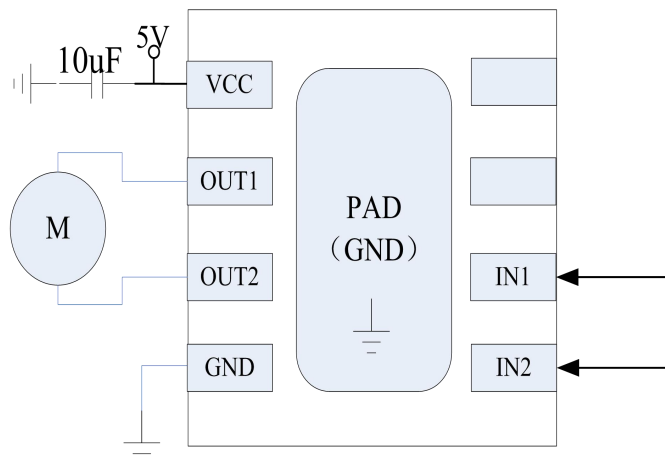


Figure 4B Application circuit Example for DFN8

Grounding

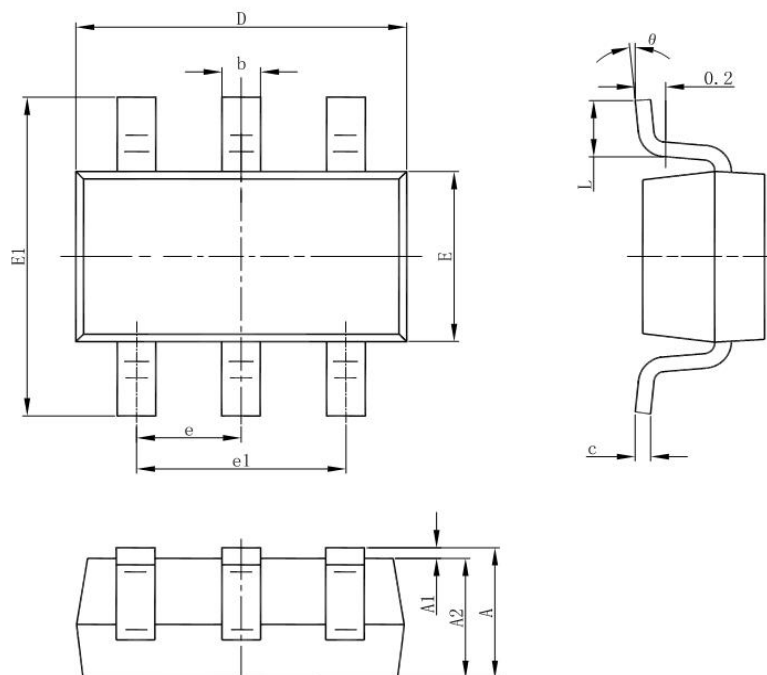
In order to minimize the effects of ground bounce and offset issues, it is important to have a low impedance singlepoint ground, known as a star ground, located very close to the device. A low impedance ground will prevent ground bounce during high current operation and ensure that the supply voltage remains stable at the input terminal.

Package Outline Dimensions

SOT-23-6L

UNIT: mm

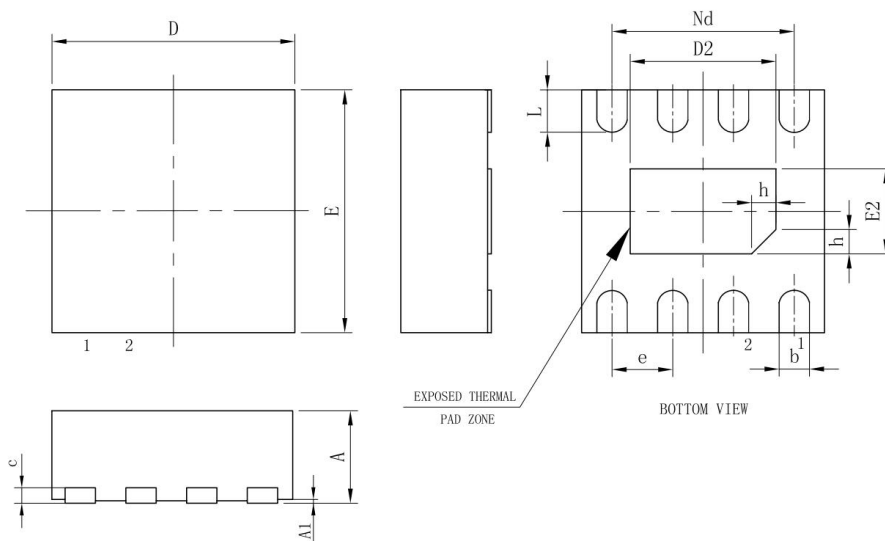
SOT-23-6L PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0	8	0	8

DFNWB2X2-8L (P0.5T0.75/0.85)

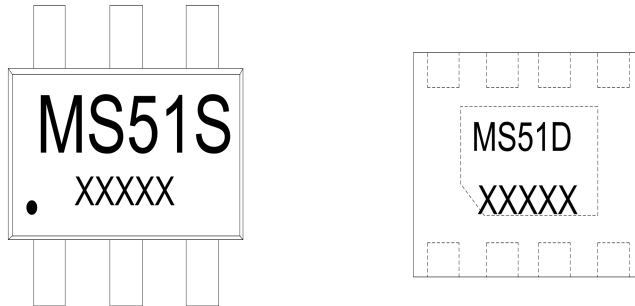
UNIT: mm



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	—	0.02	0.05
b	0.18	0.25	0.30
c	0.18	0.20	0.25
D	1.90	2.00	2.10
D2	1.10	1.20	1.30
e	0.50BSC		
Nd	1.50BSC		
E	1.90	2.00	2.10
E2	0.60	0.70	0.80
L	0.30	0.35	0.40
h	0.15	0.20	0.25
载体尺寸 (mil)	63X39		

Marking and Packaging Specifications

1、Marking drawing description



MS51S/D: product name

XXXXX : Product code

2、Marking drawing pattern

Laser printing, contents in the middle, font type Arial.

3、Packaging Specifications

Device	Package	Piece/Reel	Reel/Box	Piece/Box	Box/Carton	Piece/Carton
MS31051S	SOT23-6L	3000	10	30000	4	120000
MS31051D	DFN8	3000	10	30000	4	120000

REVISION HISTORY

Revision	Revision Date	Description	Page
V1.0	2021/01/11		

STATEMENT

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- The process of improving product is endless. And our company would sincerely provide more excellent product for customer.

**MOS CIRCUIT OPERATION PRECAUTIONS**

Static electricity can be generated in many places. The following precautions can be taken to effectively prevent the damage of MOS circuit caused by electrostatic discharge:

1. The operator shall ground through the anti-static wristband.
2. The equipment shell must be grounded.
3. The tools used in the assembly process must be grounded.
4. Must use conductor packaging or anti-static materials packaging or transportation.



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